



2811

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **U-Ming Ko**Docket No.: **TI-29632**Serial No.: **09/723,563**Examiner: **TBD**Filed: **11/28/2000**Art Unit: **2811**For: **Input/Output Architecture for Integrated Circuits**Confirmation No.: **5590**#4/a
7-14-01
Payton**PRELIMINARY AMENDMENT**

June 27, 2001

Assistant Commissioner For Patents

Washington, D. C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, D.C. 20231.

Elizabeth Austin

Date

6/29/2001

Sir:

Please amend the above-referenced application as follows:

In the Specification:

On page 1, after the title and before the first line of text, insert a new paragraph

a1 -- This application claims priority under 35 U.S.C. §119(e)(1) of provisional application Serial No. 60/175,613 filed on 01/11/2000, the entirety of which is incorporated herein by reference.--.

Respectfully submitted,

Ronald O. Neerings
Attorney for Applicant
Reg. No.: 34,227

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972) 917-5299